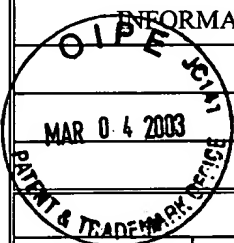


U.S. Department of Commerce, Patent and Trademark Office					Atty Docket No.		Serial No.	
					CIS0071US		09/821,948	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Applicant(s)			
(Use several sheets if necessary)					Douglas E. Duschatko, Andrew J. Thurston			
					Filing Date		Group	
					March 30, 2001		2184	
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA							
	AB							
	AC							
	AD							
	AE							
	AF							
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	AH							
	AI							
	AJ							
	AK							
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
DT	AQ	Christian Schuler, "Code Generation Tools For Hardware Implementation Of FEC Circuits," GMD FOKUS, Kaiserin-Augusta-Allee 31, 10589 Berlin, Germany.						
DT	AR	Shu Lin and Daniel J. Costello, Jr., "Error Control Coding <i>Fundamentals and Applications</i> ," Prentice-Hall, Inc. Englewood Cliffs, New Jersey, Chapter 6, pp. 141-183.						
	AS							
Examiner		Date Considered						
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